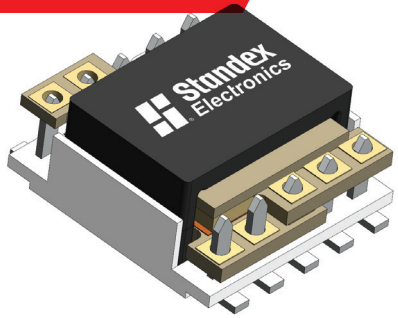
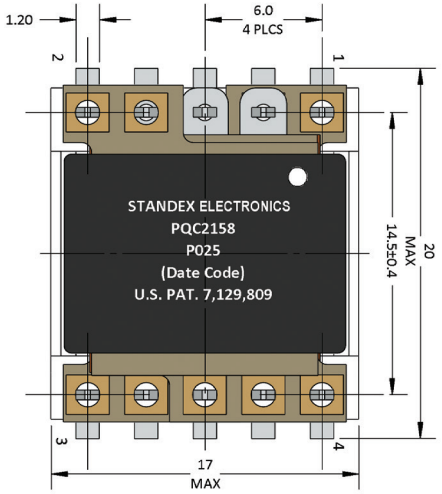
SIZE 025
10W-50W

DESIGN EXAMPLE





TRANSFORMER DESIGN | EXAMPLE - PQC2158 (U.S. PAT. 7,129,809)

ELECTRICAL SPECIFICATIONS

Topology	Forward w/Active Reset
Input Voltage	15-42VDC
Output Voltage/Current After Rectification	15VDC/2ADC
Turns Ratio - Np/Ns	6T/12T
Switching Frequency	300kHz
Duty Cycle At Low Input Voltage Max.	53.0%
Efficiency At Vin.=28Vdc/30W Output Calc.	98.2% (0.53W losses)
Operating Ambient Range (Full Load)	-20°C to +85°C

*When bonded to substrate and soldered using all available terminals.

Temp. Rise, Max.	+15°C
Minimum Isolation Voltage	
Primary To Secondary And Core	200VDC
Secondary To Core	200VDC
Primary Inductance, Np, Min.	43µH
Primary Resistance, Rdc, Np, Max.	9mOhm
Secondary Resistance, Rdc, Ns, Max.	65mOhm
Leakage Inductance 1-2/3-4 Shorted, Typ.	0.2µH
Weight Range (Approximate)	12-50grams

NOTES:

1) PATENTED HEADER AND SURFACE MOUNT TERMINATIONS PROVIDE REPEATABLE CO-PLANARITY FOR MANUFACTURING

2) THROUGH-HOLE OR SURFACE MOUNT AVAILABLE